

**What is claimed is:**

1. A method of driving a plasma display panel having first and second row electrodes and a heat electrode and  
5 including a sustain period for implementing a gray scale depending upon a discharge frequency, comprising the step of:

alternately applying first and second sustain pulses having a different width during the sustain period to the  
10 first and second row electrodes.

2. The method as claimed in claim 1, wherein a resistance going from a first driver generating the first sustain pulse into the first row electrode is different from a  
15 resistance going from a second driver generating the second sustain pulse into the second row electrode.

3. The method as claimed in claim 2, wherein said resistance going the first driver into the first row electrode is larger than a resistance going the second driver into the second row electrode.  
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4. The method as claimed in claim 3, wherein a width of the first sustain pulse is longer than that of the second sustain pulse.  
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5. The method as claimed in claim 3, wherein a sustain period of the first sustain pulse is longer than that of the second sustain pulse.

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6. The method as claimed in claim 5, wherein a rising edge caused by an energy recovering circuit of the first sustain pulse is shorter than a rising edge caused by the

energy recovering circuit of the second sustain pulse.

7. The method as claimed in claim 2, wherein a resistance going from the second driver into the second row electrode  
5 is larger than a resistance going from the first driver into the first row electrode.

8. The method as claimed in claim 7, wherein a width of the second sustain pulse is longer than that of the first  
10 sustain pulse.

9. The method as claimed in claim 7, wherein a sustain period of the second sustain pulse is longer than that of the first sustain pulse.

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10. The method as claimed in claim 9, wherein a rising edge caused by an energy recovering circuit of the second sustain pulse is shorter than a rising edge caused by the energy recovering circuit of the first sustain pulse.

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